

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Reissue Application of	:	Attn: BOX REISSUE
U.S. Patent No.5,600,672	:	Atty. Docket No. 2003_1523
Issued February 4, 1997	:	
Mitsuaki OSHIMA et al.	:	
Serial No. NEW	:	
Filed October 22, 2003	:	
COMMUNICATION SYSTEM (Reissue Continuation of Serial No. 09/688,028, Filed October 12, 2000)	:	THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

LETTER RE PROPOSED DRAWING AMENDMENTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

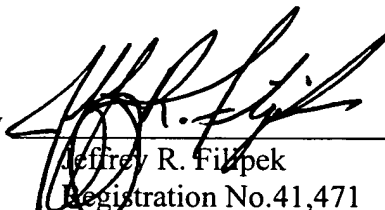
Enclosed herewith is a photocopy of Figs. 2, 10, 17, 29, 48, 65, 67, 93, 112, 131, 138, 144, and 169 marked in red to indicate proposed drawing amendments thereto.

The Examiner is requested to approve such proposed drawing amendments. Formal drawings incorporating such amendments are filed herewith.

Respectfully submitted,

Mitsuaki OSHIMA et al.

By



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October 22, 2003

FIG. 2

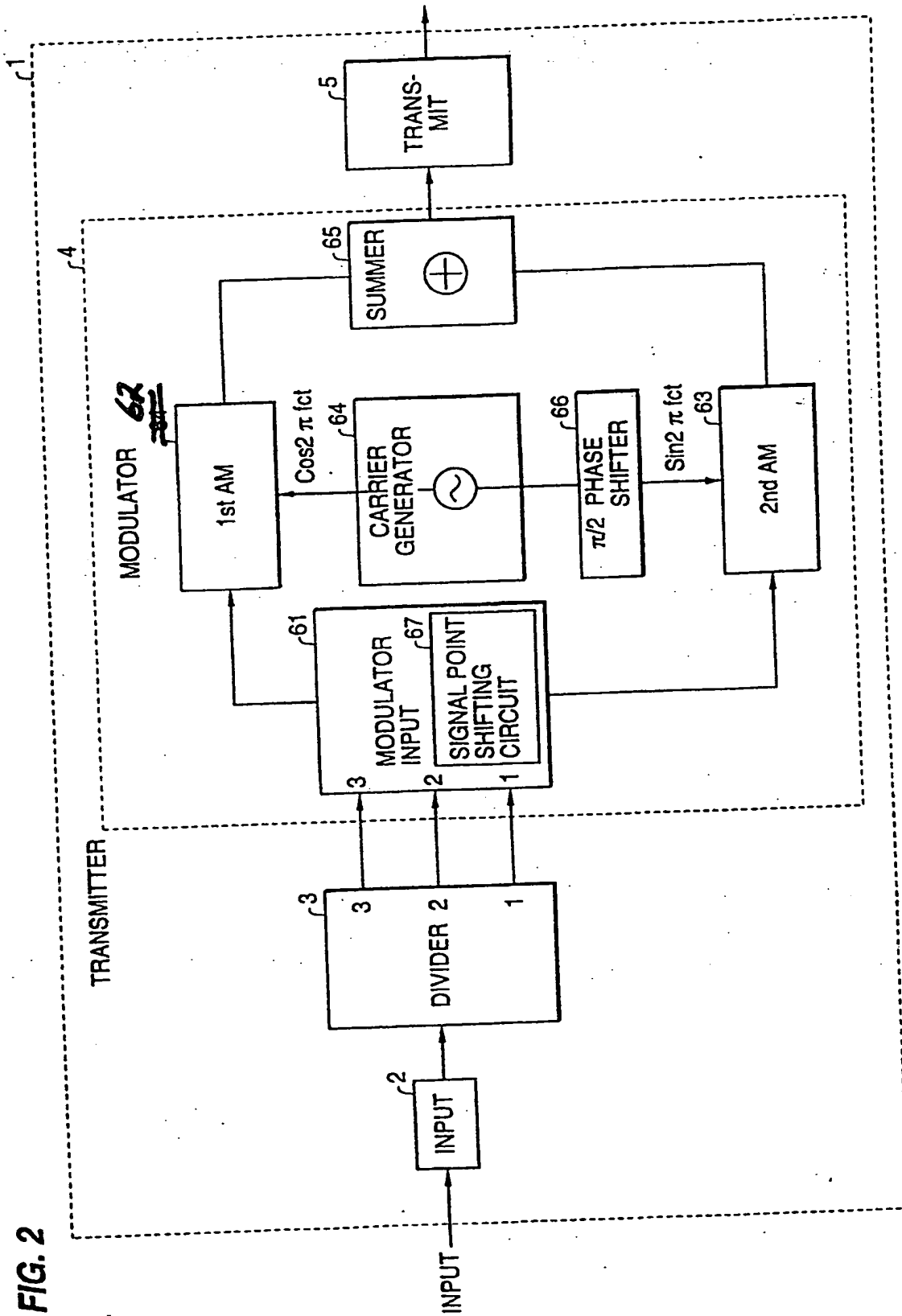


FIG. 10

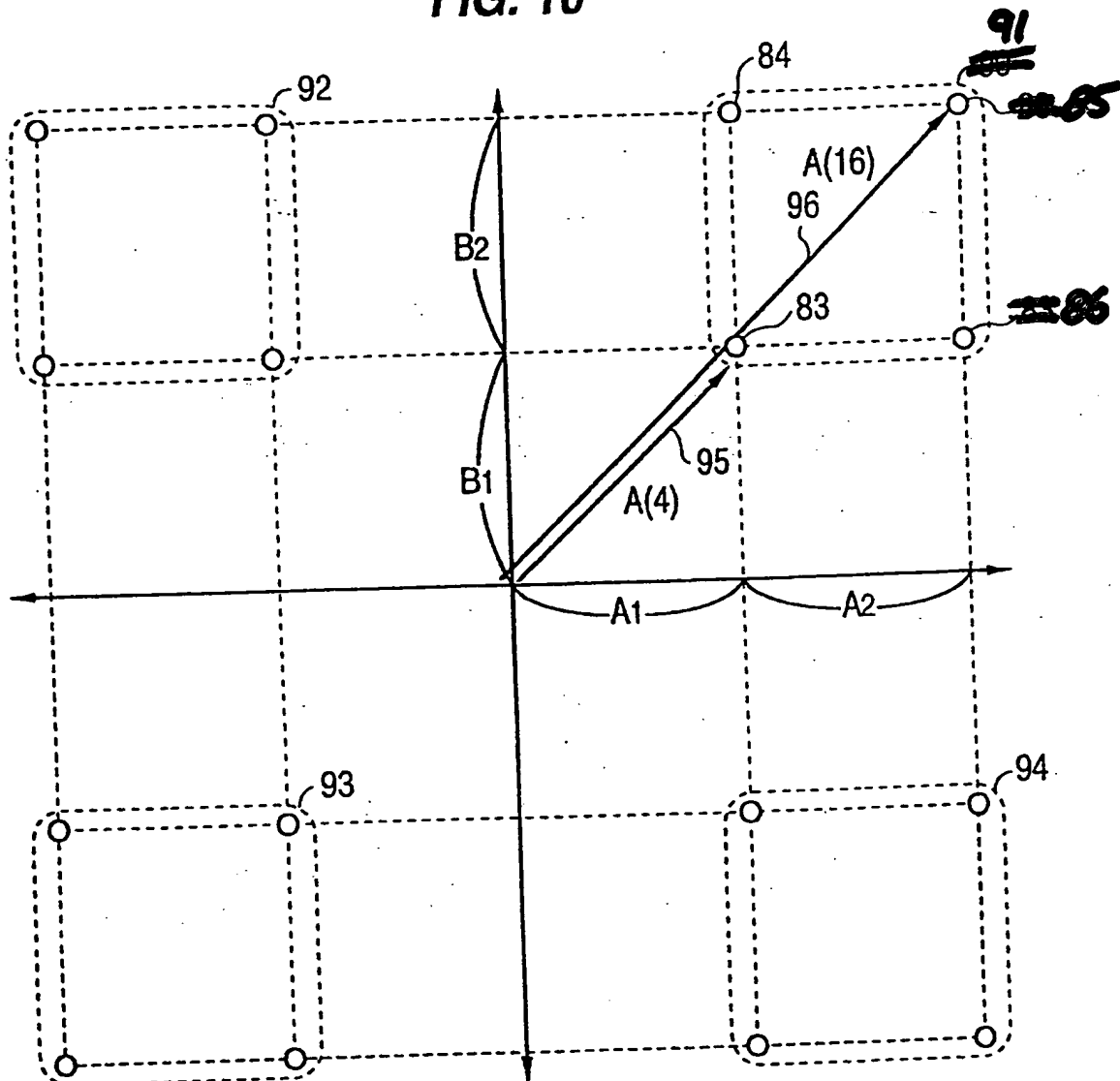
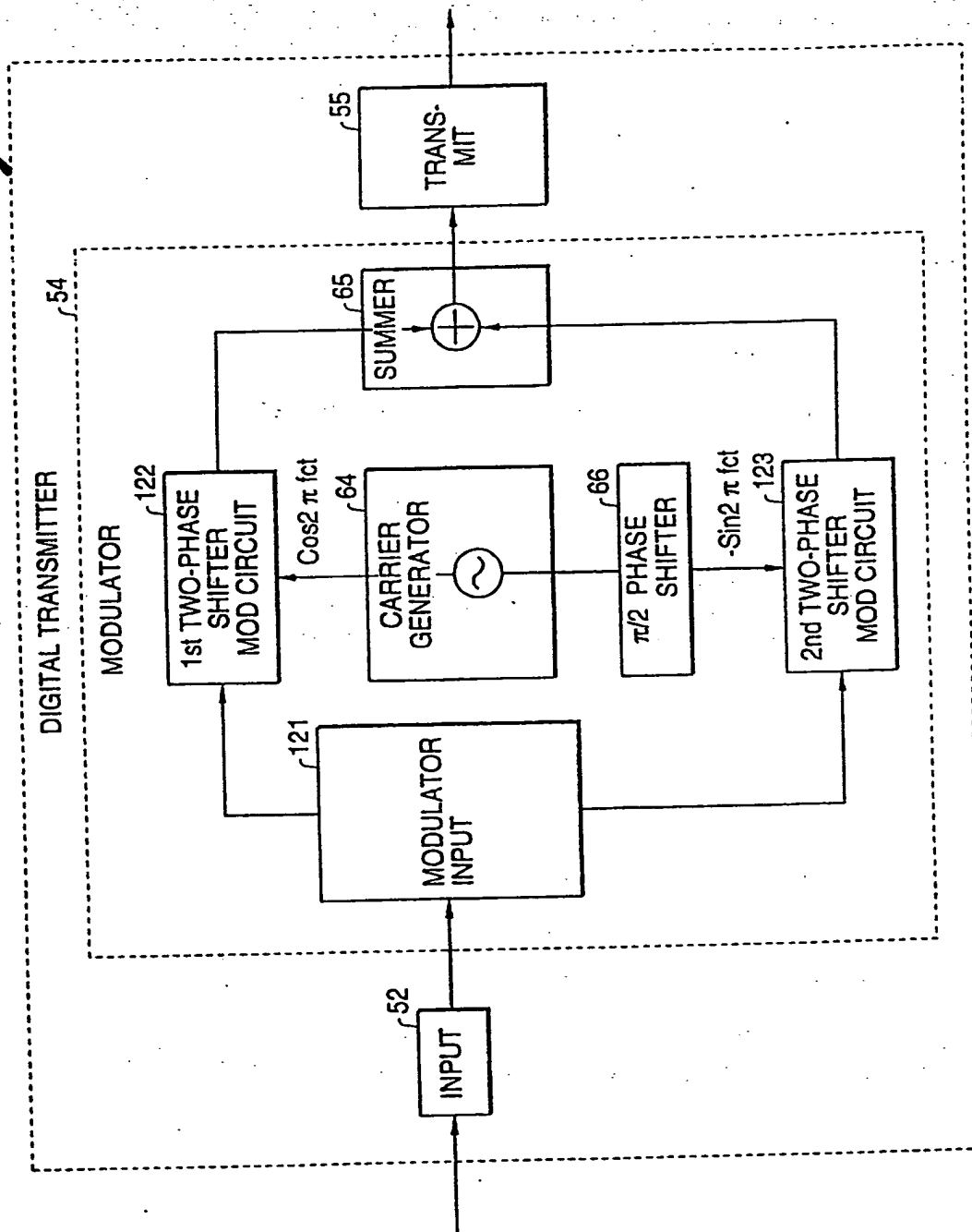


FIG. 17



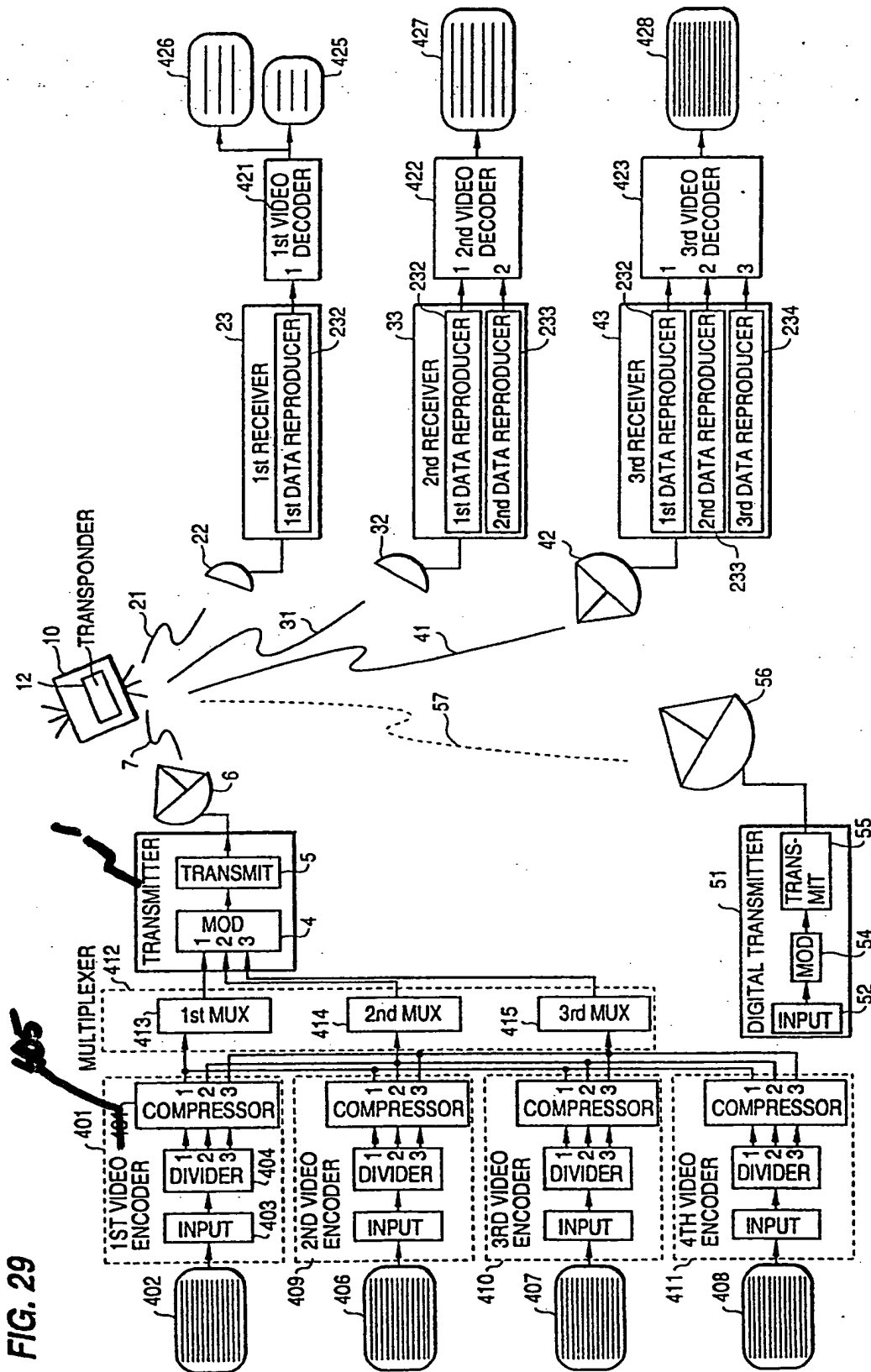


FIG. 48

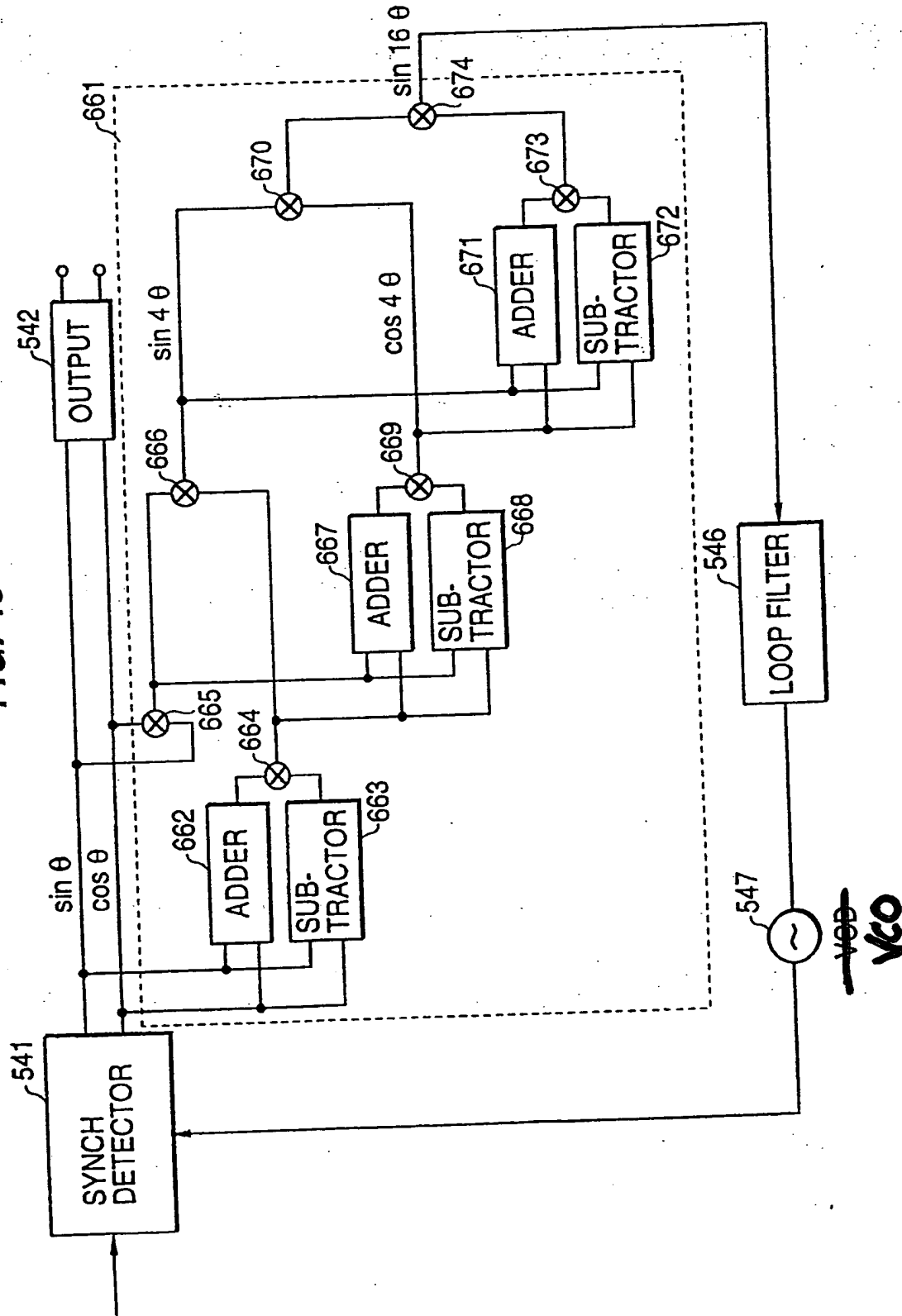
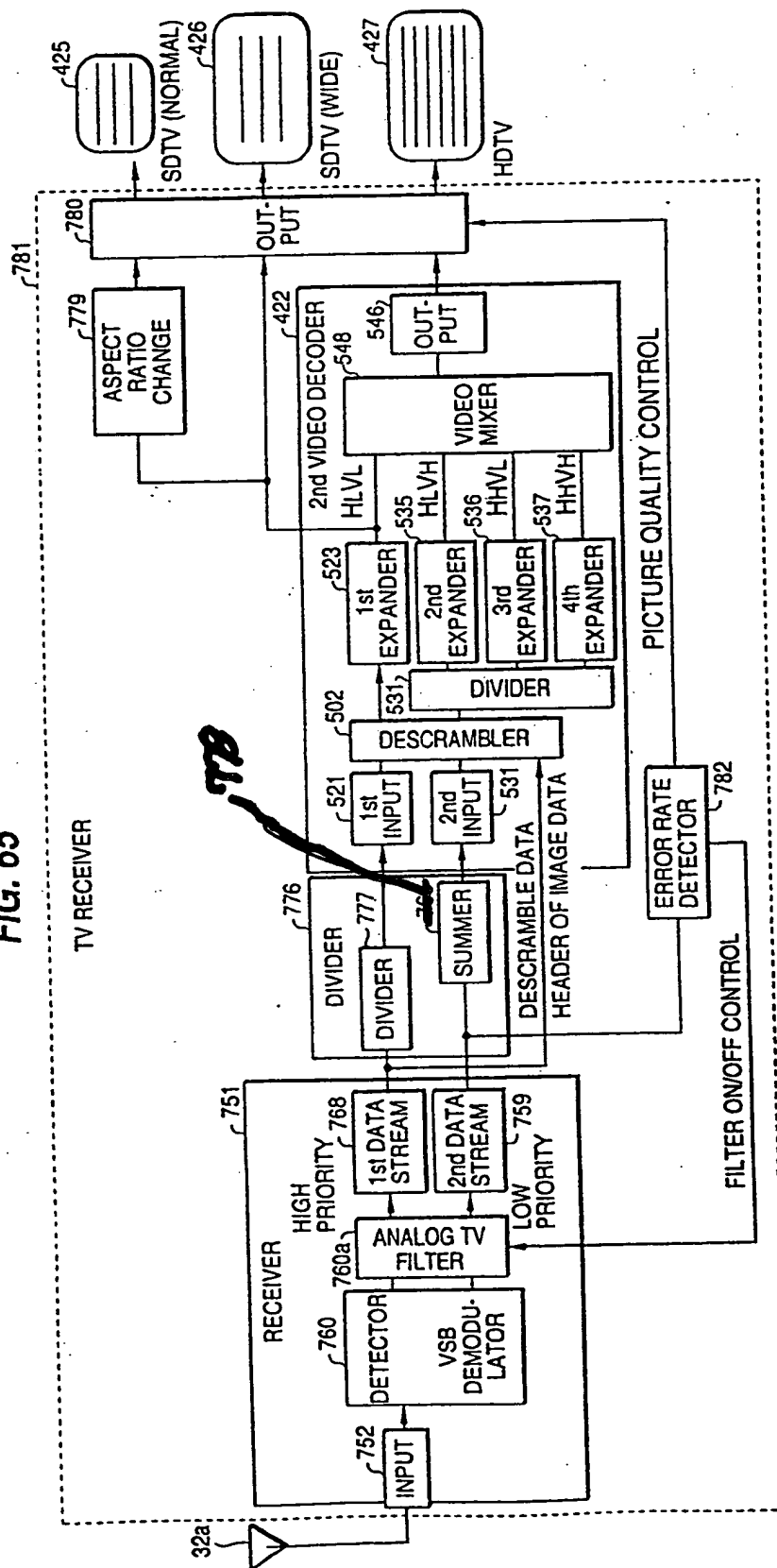


FIG. 65



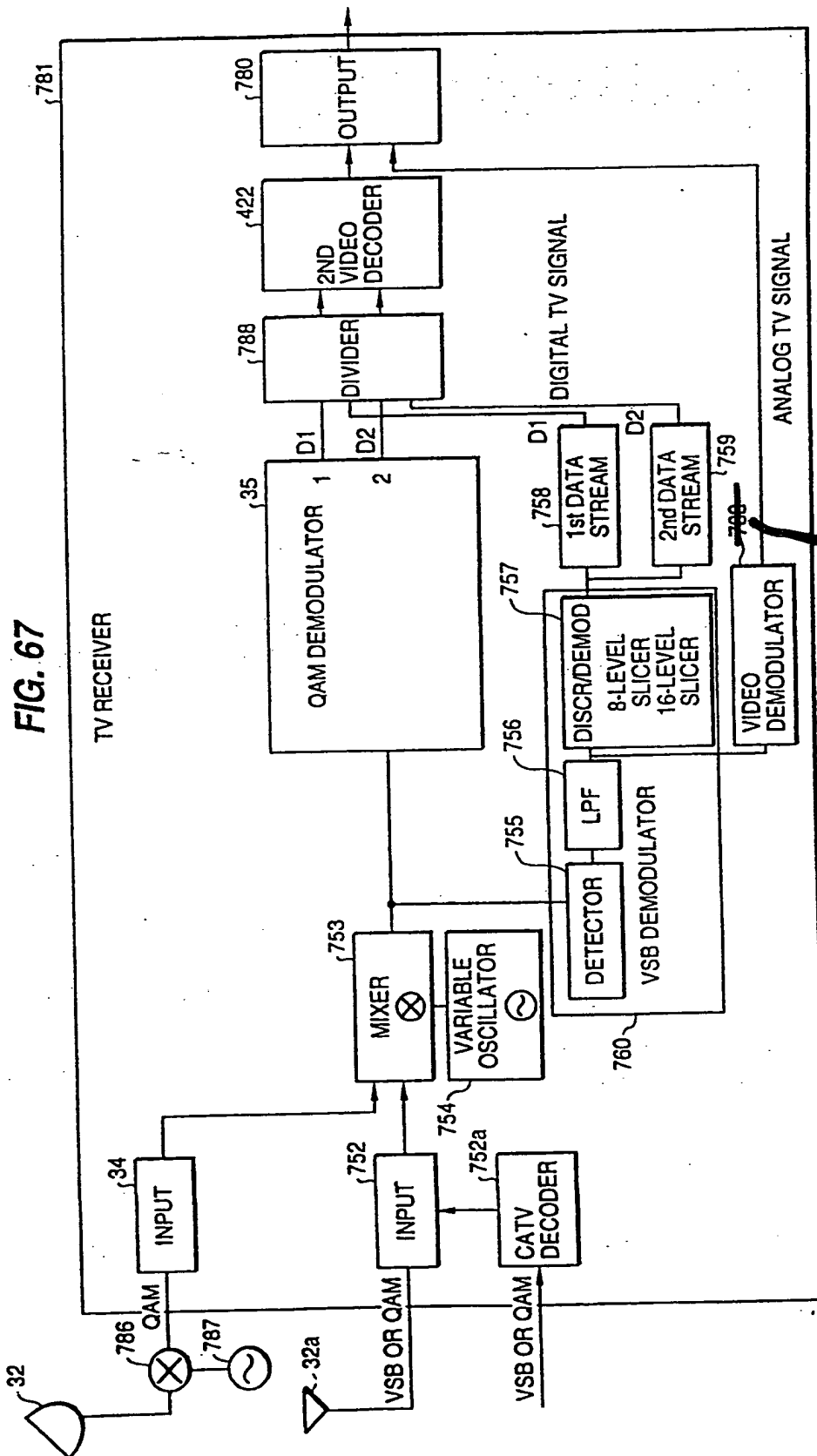


FIG. 93

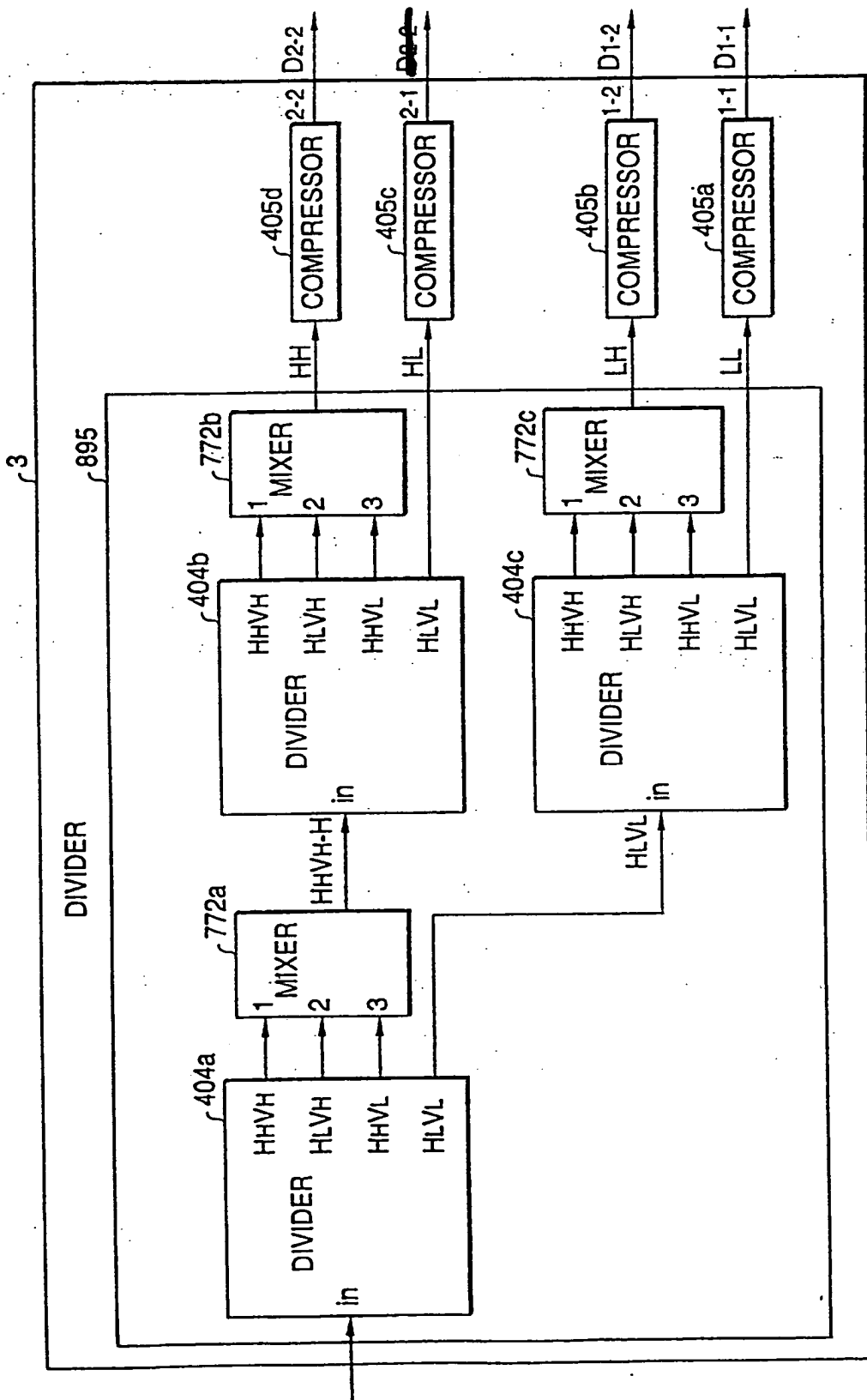
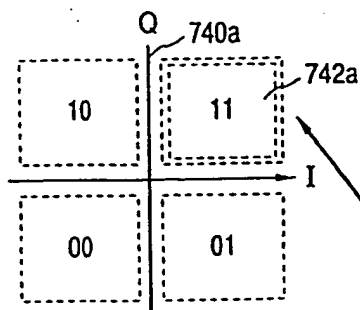
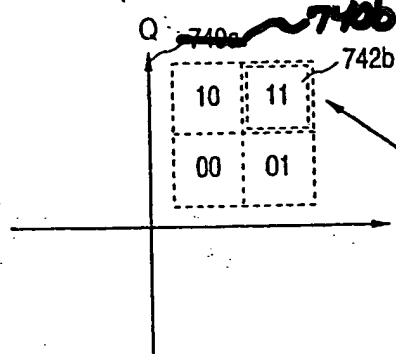


FIG. 112

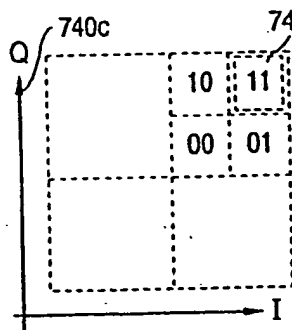
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



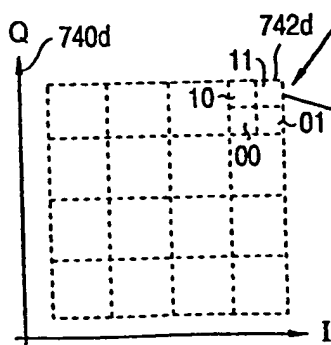
SUBCHANNEL-2 (16-SRQAM:D2 = 2bit)



SUBCHANNEL-3 (64-SRQAM:D3 = 2bit)



SUBCHANNEL-4 (256-SRQAM:D4 = 2bit)

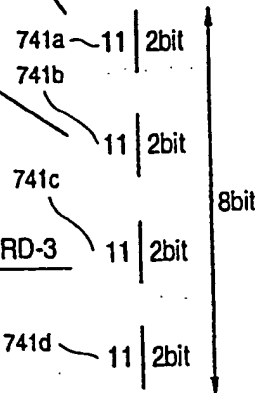


CODE WORD-1

CODE WORD-2

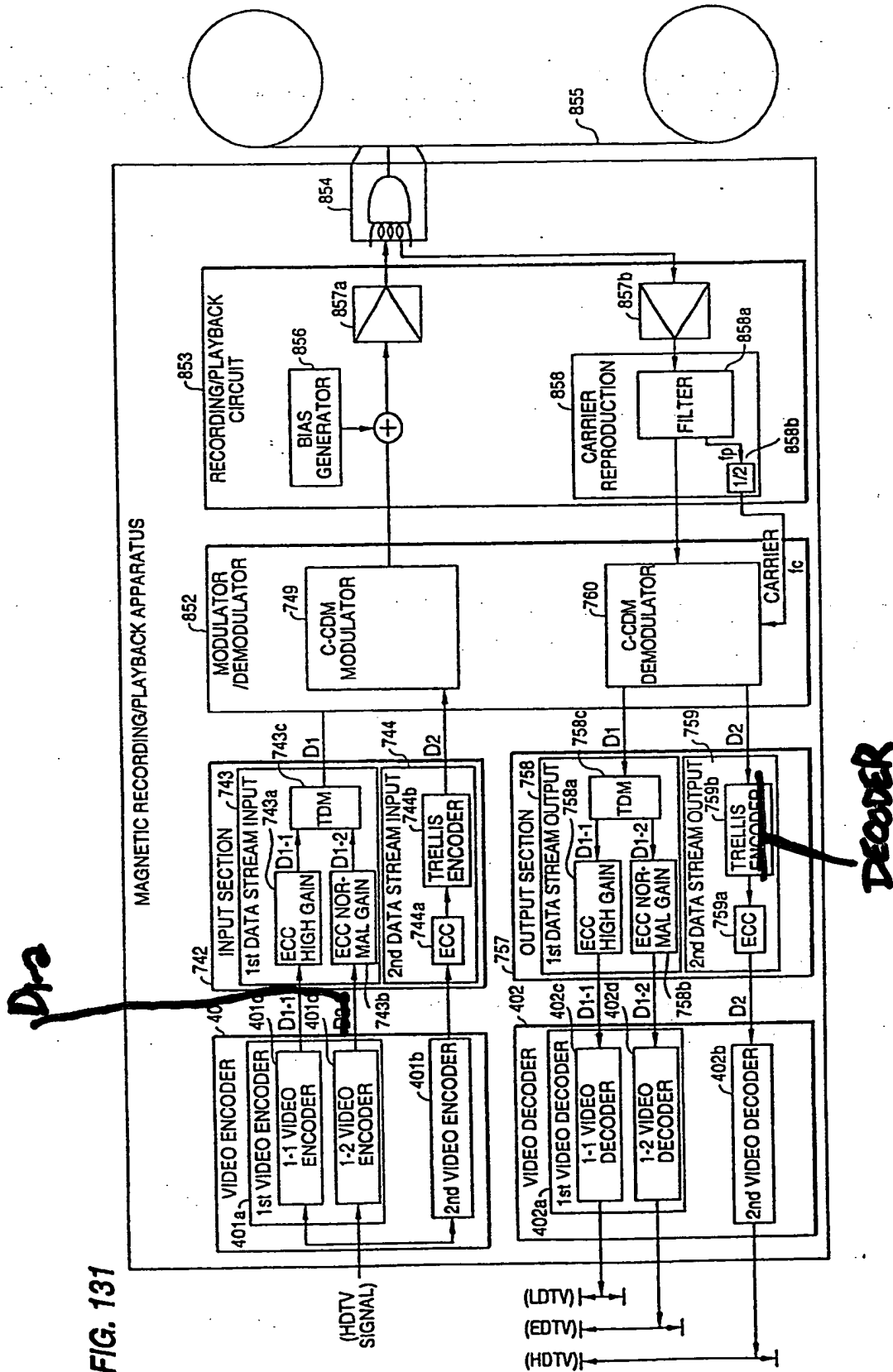
CODE WORD-3

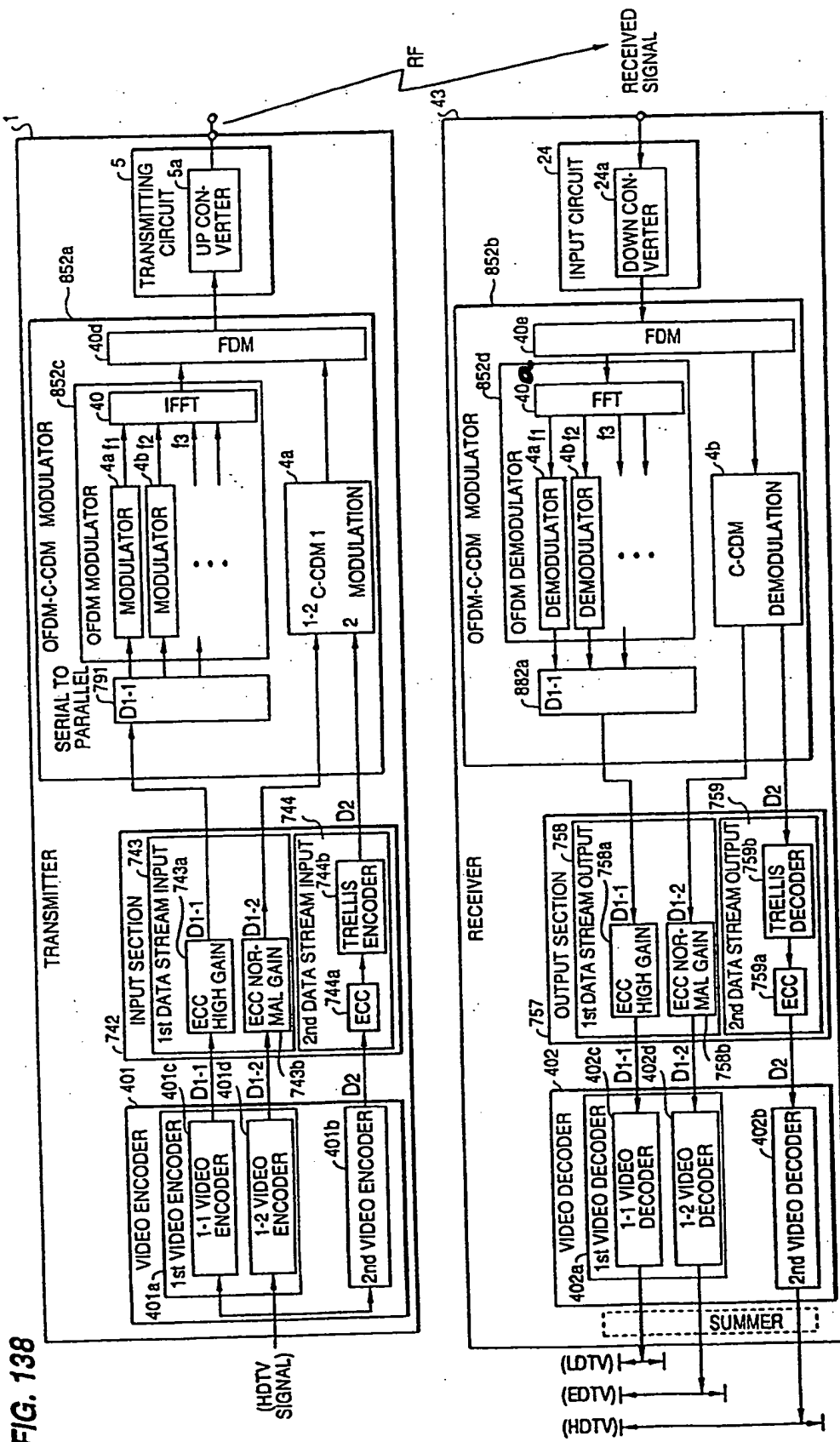
CODE WORD-4



SIGNAL POINT
CODE WORD
11 11 11 11

FIG. 131





The diagram illustrates a transmitter and receiver system for HDTV signals, organized into two main sections: TRANSMITTER and RECEIVER.

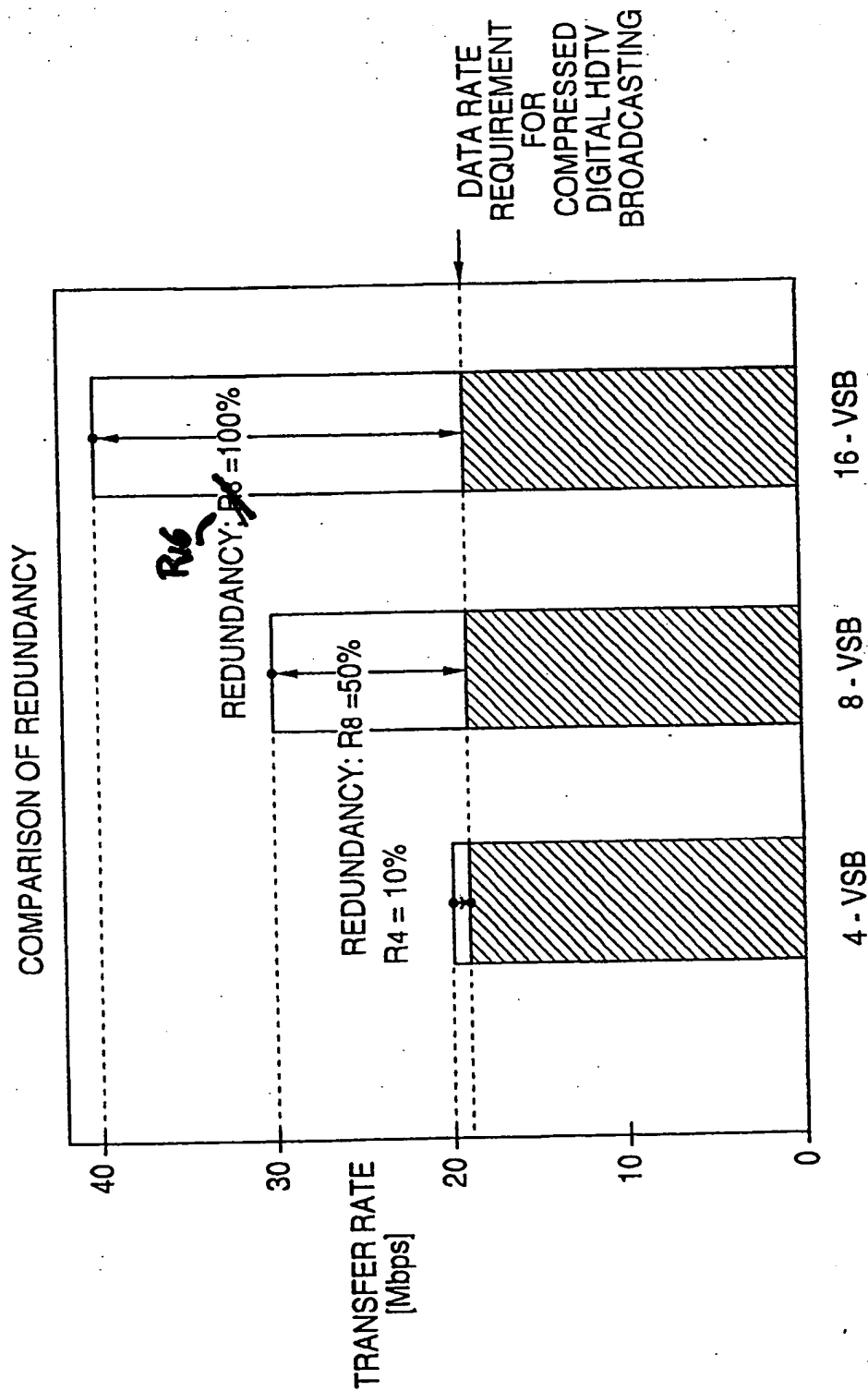
TRANSMITTER:

- Input Section (742):** Receives an (HDTV SIGNAL). It splits into two paths:
 - 1st DATA STREAM INPUT (743a):** Passes through an ECC HIGH GAIN block (D1-1) and an ECC NOR-MAL GAIN block (D1-2) before entering a TDM block.
 - 2nd DATA STREAM INPUT (744b):** Passes through an ECC block (D2) before entering a TRELLIS ENCODER block.
- Video Encoder (401):** Processes video data through:
 - 1st VIDEO ENCODER (401a):** Contains a 1-1 VIDEO ENCODER (D1-1) and a 1-2 VIDEO ENCODER (D1-2).
 - 2nd VIDEO ENCODER (401b):** Receives input from the 1-2 VIDEO ENCODER (D1-2) and the TRELLIS ENCODER.
- Weighted OFDM-Modulator (4):** Receives data from the video encoder and the 1st data stream input. It includes a D2 SERIAL TO PARALLEL block and a WEIGHTED MODULATOR (4a f1 to 4e f7) with individual MOD. blocks.
- IFFT (40) and D/A CONVERTER (4e):** Process the modulated signal.
- Transmit Circuit (5a):** Includes an UP CON-VERTER and a TRANSMIT CIRCUIT, outputting an RF signal.

RECEIVER:

- Weighted OFDM-Demodulator (45):** Receives a RECEIVED SIGNAL. It includes a D2 block and a WEIGHTED DEMOD. block (45a f1 to 45e f7) with individual DEMOD. blocks.
- A/D CONVERTER (40c):** Converts the demodulated signal to digital.
- FFT (40a):** Processes the digital signal.
- Input Circuit (24a):** Includes a DOWN CON-VERTER and an INPUT CIRCUIT.
- Output Section (757):** Processes the received data:
 - 1st DATA STREAM OUTPUT (758a):** Passes through an ECC HIGH GAIN block (D1-1) and an ECC NOR-MAL GAIN block (D1-2) before entering a TDM block.
 - 2nd DATA STREAM OUTPUT (759b):** Passes through an ECC block (D2) before entering a TRELLIS DECODER block.
- Video Decoder (402):** Processes video data through:
 - 1st VIDEO DECODER (402a):** Contains a 1-1 VIDEO DECODER (D1-1) and a 1-2 VIDEO DECODER (D1-2).
 - 2nd VIDEO DECODER (402b):** Receives input from the 1-2 VIDEO DECODER (D1-2) and the TRELLIS DECODER.
- Summer:** Combines the outputs of the video decoder and the 1st data stream output.

FIG. 169



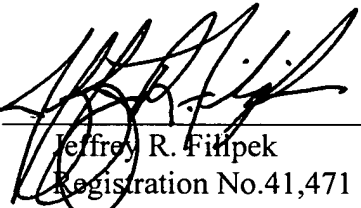
REMARKS

By this preliminary amendment, claims 1-12 have been canceled and claims 13-23 added. Thus, claims 13-23 are active. Support for the new claim recitations can be found at least at: Fig. 65; Fig. 66; and column 49, lines 25-32. If the Examiner requires further supporting passages, she is invited to contact the undersigned by telephone.

The present Preliminary Amendment seeks to amend the specification by replacing the specification and abstract with the enclosed substitute specification and abstract. Since this is a reissue application, the substitute specification includes bracketing and underlining to show the changes relative to the patent specification.

Respectfully submitted,

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